



## TENTATIVE TEACHING PLAN

Name of Teacher: **Dr. Abdul Latif**

Subject: **Linear Integrated Circuit & Filters**

Batch: 15TL (I&II) Year: 2<sup>nd</sup> Term: 2<sup>nd</sup>

Term Starting Date: 18-07-2016

Term Suspension date: 06-11-2016

S. No.	Topic	No. of Lectures required
1	Introduction to Integrated Circuit and Filters	1
2	Basics of Operational Amplifiers	2
3	Interpreting Op-amp Data sheet	1
4	Specification of Op-amp	3
5	Common Mode Rejection Ratio , Slew Rate	2
6	Op-amp Frequency response , Gain Bandwidth product	1
7	Inverting, Non Inverting & voltage follower Circuit With Design problems	2
8	Summer, Differential Op-Amp & Its Design Problems+ Class Test# 01	2
9	Differentiators, Integrators & Its Design Problems	2
10	Logarithmic, Anti Logarithmic Amplifier & Its Design Problems	2
11	Precision Rectifiers	2
12	Peak Detectors	1
13	Comparators, Schmitt trigger and voltage limiter using op-amp	2
14	Clampers	1
15	Sample & Hold circuit	1
16	D/A Converters+ Class Test # 02	2
17	A/D Converters	2
18	Oscillators	2
19	Integrated Timer	3
20	Introduction to Filters (Filters Basic Terminology + Filter Response Terminology)	2
21	Butterworth, Chebyshev, Elliptic, and Bessel Approximation	2
22	First & Second Order Lowpass and Highpass Filters	2
23	First & Second Order Bandpass and Bandstop Filters + Class Test # 03	2
<b>Total Lectures</b>		<b>42</b>

Signature of Teacher

Dated

Remarks of DMRC

Signature of Chairman/ Director

Dated