



TENTATIVE TEACHING PLAN

Name of Teacher: **Mansoor Ali**

Subject: **Basic Electronics**

Term Starting Date: 11-07-2016

Batch: 16TL Year: 1st Term: 2nd

Term Suspension Date: 01-10-2016

S. No.	Topic	No. of lectures
01.	Introduction to the subject	01
02.	Review of Basic Solid state physics concepts: Atomic structure of elements, energy level diagram for solids.	02
03.	Conduction in solids, Electron hole pair, Recombination & lifetime	01
04.	Doping, Intrinsic and Extrinsic semiconductors	01
05.	Bias, Forward Bias & Reverse Bias	01
06.	PN junction, ideal diode & real diode	02
07.	Special Purpose Diodes: Zener diode	01
08.	Schottky diode, tunnel diode, varactor diode, LED & laser diode.	02
09.	Diode Applications: Half wave rectification & full wave rectification	02
10.	Clipper & Clamper circuits	02
11.	Voltage multipliers / class test # 1	01
12.	Bipolar Junction Transistor	02
13.	Common base configuration	01
14.	Common emitter configuration	01
15.	Common collector configuration	01
16.	Static characteristics, Q-point amplification.	02
17.	D.C. circuit analysis, transistors as a switch.	02
18.	Biasing circuits, fixed bias method.	01
19.	Feedback resistor biasing, voltage divider bias method. / Class test # 2	02
20.	Junction FET, Output characteristics.	02
21.	Transfer characteristics of JFET	02
22.	n-channel and p-channel JFET	02
23.	Biasing circuits	02
24.	MOSFET, D-MOSFET and E-MOSFET	02
25.	Transfer characteristics	01
26.	Network Theorems/ Class test # 3	03
	Total lectures	42

Signature of teacher:

Dated:

Remark of DMRC:

Signature of chairman:

Dated: